Kleenex: Compiling Nondeterministic Transducers to Deterministic Streaming Transducers

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Abstract
We present and illustrate Kleenex, a language for expressing general nondeterministic finite transducers, and its novel compilation to streaming string transducers with worst-case linear-time performance and sustained high throughput. Its underlying theory is based on transducer decomposition into oracle and action machines: the oracle machine performs streaming greedy disambiguation of the input; the action machine performs the output actions. In use cases Kleenex achieves consistently high throughput rates around the 1 Gbps range on stock hardware. It performs well, especially in complex use cases, in comparison to both specialized and related tools such as AWK, sed, RE2, Ragel and regular-expression libraries.

Categories and Subject Descriptors D.3.1 [Formal Definitions and Theory]: Semantics; D.3.2 [Language Classifications]: Specialized application languages; F.1.1 [Models of Computation]: Automata

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1. Introduction
A Kleenex program consists of a context-free grammar, restricted to guarantee regularity, with embedded side-effecting semantic actions.

We illustrate Kleenex by an example. Consider a large text file containing unbounded numerals, which we want to make more readable by inserting separators; e.g. “12742” is to be replaced by “12,742”). In Kleenex, this transformation can be specified as follows:

```plaintext
main := (num /[0-9]/ | other)*
num := digit{1,3} ("," digit{3})*
digit := /[0-9]/
other := /./
```

This is the complete program. The program defines a set of nonterminals, with `main` being the start symbol. The constructs `/[0-9]/`, `/[0-9]/` and `/./` specify matching a single digit, any non-digit and any symbol, respectively, and echoing the matched symbol to the output. The construct "," reads nothing and outputs a single comma. The star * performs the inner transformation zero or more times; the repetition `{1,3}` performs it between 1 and 3 times. Finally, the | operator denotes prioritized choice, with priority given to the left alternative. An example of its execution is as follows:

<table>
<thead>
<tr>
<th>Input read so far</th>
<th>… and output produced so far</th>
</tr>
</thead>
<tbody>
<tr>
<td>Surf</td>
<td>Surf</td>
</tr>
<tr>
<td>Surface::14479</td>
<td>Surface::14479</td>
</tr>
<tr>
<td>Surface::1447985</td>
<td>Surface::1447985</td>
</tr>
<tr>
<td>Surface::144798500</td>
<td>Surface::144,798,500</td>
</tr>
<tr>
<td>Surface::144798500_km2</td>
<td>Surface::144,798,500_km2</td>
</tr>
</tbody>
</table>

The example highlights the following:

Ambiguity by design. Any string is accepted by this program, since any string matching `num /[^0-9]/` also matches (other)*.

Greedy disambiguation forces the `num /[^0-9]/` transformation to be tried first, however, and only if that fails do we fall back to echoing the input verbatim to the output using other.

Streaming output. The program almost always detects the earliest possible time an output action can be performed. Any non-digit symbol is written to the output immediately, and as soon as the first non-digit symbol after a sequence of digits is read, the resulting numeral with separators is written to the output stream. The first of a sequence of digits is not output right away, however. Employing a strategy that always outputs as early as possible would require solving a PSPACE-hard problem.

A Kleenex program is first compiled to a possibly ambiguous (finite-state) transducer. Any transducer can be decomposed into two transducers: an oracle machine, which maps an input string to a bit-coded representation of the transducer paths accepting the input, and a deterministic action machine, which translates such a bit-code to the corresponding sequence of output actions in the original transducer. The greedy leftmost path in the oracle machine corresponds to the lexicographically least bit-code of paths accepting a given input; consequently, disambiguation reduces to computing this bit-code for a given input. To compute it, the oracle machine is simulated in a streaming fashion. This generalizes NFA simulation to not just yield a single-bit output—accept or reject—but also the lexicographically least path witnessing acceptance. The simulation algorithm maintains a path tree from the initial state to all the oracle machine states reachable by the input prefix read so far. A branching node represents both sides of an alternative where both are still viable. The output actions on the (possibly empty) path segment from the initial state to the first branching node can be performed based on the input prefix processed so far without knowing which of the presently reached states will eventually accept
the rest of the input. This algorithm generalizes greedy regular expression parsing [31, 32] to arbitrary right-regular grammars. Regular expressions correspond to certain well-structured oracle machines via their McNaughton-Yamada-Thompson construction. The simulation algorithm automatically results in constant memory space consumption for grammars that are deterministic modulo finite lookahead, e.g. one-unambiguous regular expressions [19]. For arbitrary transducers the simulation requires linear space in the size of the input in the worst case. No algorithm can guarantee constant space consumption: the number of unique path trees computed by the streaming algorithm is potentially unbounded due to the possibility of arbitrarily much lookahead required to determine which of two possible alternatives will eventually succeed. Unbounded lookahead is the reason that not all unambiguous transducers can be deterministic to a finite state machine [13, 53].

By identifying path trees with the same ordered leaves and underlying branching structure, we obtain an equivalence relation with finite index. That is, a path tree can be seen as a rooted full binary tree together with an association of output strings with tree edges, and the set of reachable root full binary trees of an oracle machine can be precomputed analogous to the NFA state sets reachable in an NFA. We can thus compile an oracle machine to a streaming string transducer [4, 5, 9], a deterministic machine model with (unbounded sized) string registers and affine (copy-free) updates associated with each transition: a path tree is represented as an abstract state and the contents of a finite set of registers, each containing a bit sequence coding a path segment of the represented path tree. Upon reading an input, the state is changed and the registers are updated in-place to represent the subsequent path tree. This yields a both asymptotically and practically very efficient implementation: the example shown earlier compiles to an efficient C program that operates with sustained high throughput in the 1 Gbps range on stock desktop hardware.

The semantic model of context-free grammars with unbridled “regular” ambiguity and embedded semantic actions is flexible and the above implementation technology is quite general. For example, the action transducer is not constrained to producing output in the string monoid, but can be extended to any monoid. By considering the monoid of affine register updates, Kleenex can code all nondeterministic streaming string transducers [6].

1 Contributions

This paper makes the following novel contributions:

- A streaming algorithm for nondeterministic finite state transducers (FST), which emits the lexicographically least output sequence generated by all accepting paths of an input string based on decomposition into an input-processing oracle machine and an output-effecting action machine. It runs in $O(mn)$ time for transducers of size $m$ and inputs of size $n$.

- An effective determination of FSTs into a subclass of streaming string transducers (SST) [4], finite state machines with copy-free updating of string registers when entering a new state upon reading an input symbol.

- An expressive declarative language, Kleenex, for specifying FSTs with full support for and clear semantics of unrestricted nondeterminism by greedy disambiguation. A basic Kleenex program is a context-free grammar with embedded semantic output actions, but syntactically restricted to ensure that the input is regular.1 Basic Kleenex programs can be functionally composed into pipelines. The central technical aspect of Kleenex is its semantic support for unbridled nondeterminism and its effective determination and compilation to SSTs, which both highlights and complements the significance of SSTs as a deterministic machine model.

- An implementation, including empirically evaluated optimizations, of Kleenex that generates SSTs and deterministic finite-state machines, each rendered as standard single-threaded C-code that is eventually compiled to x86 machine code. The optimizations illustrate the design and implementation flexibility obtained by the underlying theories of FSTs and SSTs.

- Use cases that illustrate the expressive power of Kleenex, and a performance comparison with related tools, including Regel [65], RE2 [62] and specialized string processing tools. These document Kleenex’s consistently high performance (typically around 1 Gbps, single core, on stock hardware) even when compared to less expressive tools with special-cased algorithms and to tools with no or limited support for nondeterminism.

1.2 Overview of paper

In Section 2 we introduce normalized transducers with explicit deterministic and nondeterministic $\epsilon$-transitions. Kleenex and its translation to such transducers is defined in Section 3. We then devise an efficient streaming transducer simulation (Section 4) and its determinization (Section 5) to streaming string transducers. In Section 6 we briefly describe the compilation to C-code and some optimizations, and we then empirically evaluate the implementation on a number of simple benchmarks and more realistic use cases (Section 7). We conclude with a discussion of related and possible future work (Section 8).

We assume basic knowledge of automata [39], compilation [2], and algorithms [21]. Basic results in these areas are not explicitly cited.

2. Transducers

An alphabet $A$ is a finite set; e.g. the binary alphabet $\Sigma = \{0, 1\}$ and the empty alphabet $\emptyset = \{\}$, $A^*$ denotes the free monoid generated by $A$, that is the strings over $A$ with concatenation, expressed by juxtaposition, and the empty string $\epsilon$ as neutral element. We write $A^*[x, \ldots ]$ for extending $A$ with additional elements $x, \ldots$ not in $A$.

Definition 1 (Finite state transducer). A finite state transducer (FST) $T$ over $\Sigma$ and $\Gamma$ is a tuple $(\Sigma, \Gamma, Q, q_0, q_f, E)$ where

- $\Sigma$ and $\Gamma$ are alphabets;
- $Q$ is a finite set of states;
- $q_0, q_f \in Q$ are the initial and final states, respectively;
- $E : Q \times \Sigma^* \times \Gamma^* \times Q \times Q$ is the transition relation.

Its size is the cardinality of its transition relation: $|T| = |E|$. $T$ is deterministic if for all $q \in Q$, $a \in \Sigma^*$ we have

$(q, a, b', q') \in E \iff (q, a, b'', q') \in E \implies b' = b'' \land q' = q''$

$(q, \epsilon, b', q') \in E \iff (q, a, b'', q') \in E \implies \epsilon = a$

The support of a state is the set of symbols it has transitions on: $\text{supp}(q) = \{ a \in \Sigma^* | \exists q', b. (q, a, b, q') \in E \}$.

Deterministic FSTs with no $\epsilon$-transitions and $\text{supp}(q) = \Sigma$ for all $q$ are Mealy machines. Conversely, every deterministic FST is easily turned into a Mealy machine by adding a failure state and transitions to it. We write $q \xrightarrow{a/b} q'$ whenever $(q, a, b, q') \in E$, and $E$ is understood from the context. A path in $T$ is a possibly empty sequence of transitions

$q_0 \xrightarrow{a_1} q_1 \xrightarrow{a_2} q_2 \cdots \xrightarrow{a_n} q_n$

It has input $u = a_1 a_2 \ldots a_n$ and output $v = b_1 b_2 \ldots b_n$. We write $q_0 \xrightarrow{a_1/a_2/\ldots/a_n} q_n$ if there exists such a path.

---

1 This avoids the $\Omega(M(n))$ lower bound for context-free grammar parsing, where $M(n)$ is the complexity of multiplying $n \times n$ matrices [40].
Definition 2 (Relational semantics, input language). FST $T$ denotes the binary relation

$$\mathcal{R}[T] = \{(\tau, \tau') | q \xrightarrow{u/v} q'\}$$

where the $\epsilon$-erasure $\tau : \Sigma[\epsilon]^* \rightarrow \Sigma^*$ is $\tau = \epsilon$ and $\pi = a$ for all $a \in \Sigma$, extended homomorphically to strings. Its input language is

$$\mathcal{L}[T] = \{s | \exists t . (s, t) \in \mathcal{R}[T]\}.$$ 

Two FSTs are equivalent if they have the same relational semantics.

The class of relations denoted by FSTs are the rational relations; their input languages are the regular languages [13].

Definition 3 (Normalized FST). A normalized finite state transducer over $\Sigma$ and $\Gamma$ is a deterministic FST over $\Sigma[\epsilon_0, \epsilon_1]$ and $\Gamma$ such that for all $q \in Q$, $q$ is:

- a choice state: $\text{supp}(q) = \{\epsilon_0, \epsilon_1\}$ and $q \neq q'$, or
- a skip state: $\text{supp}(q) = \{\epsilon\}$ and $q \neq q'$, or
- a symbol state: $\text{supp}(q) = \{a\}$ for some $a \in \Sigma$ and $q \neq q'$, or
- the final state: $\text{supp}(q) = \{\}$ and $q = q'$.

We say that $q$ is a resting state if $q$ is either a symbol state or the final state.

The relational semantics $\mathcal{R}[T]$ of a normalized FST is the same as in Definition 2, where $\epsilon$-erasure is extended by $\tau_0 = \tau_1 = \epsilon$.

Proposition 1. For every FST of size $m$ there exists an equivalent normalized FST of size at most $3m$. Conversely, for every normalized FST of size $m$ there exists an equivalent FST of the same size.

Proof. (Sketch) For each state $q$ with $k > 1$ outgoing transitions, add $k$ new states $q^{(1)}, \ldots, q^{(k)}$, replace the $i$-th outgoing transition $(q, a, b, q')$ by $(q^{(i)}, a, b, q')$ and add a full binary tree of $\epsilon_0$- and $\epsilon_1$-transitions for reaching each $q^{(i)}$ from $q$. In the converse direction, replace $\epsilon_0$ and $\epsilon_1$ by $\epsilon$.

Normal FSTs are useful by limiting transition outdegree to 2, having explicit $\epsilon$-transitions and classifying them into deterministic ($\epsilon$) and ordered nondeterministic ones ($\epsilon_0, \epsilon_1$).

Proviso. Henceforth we will call normalized FSTs simply transducers.

Let $|\_| : \Sigma[\epsilon_0, \epsilon_1, \epsilon] \rightarrow 2[\epsilon]$ be defined by $|\epsilon_0| = 0, |\epsilon_1| = 1$ and $|a| = \epsilon$ for all $a \in \Sigma[\epsilon]$.

Definition 4 (Oracle and action machines). Let $T$ be a transducer. The oracle machine $T^\alpha$ is defined as $T$, but with each transition $(q, a, b, q')$ replaced by $(q, a, |a|, q')$. Its action machine $T^\beta$ is $T$, but with each transition $(q, a, b, q')$ replaced by $(q, |a|, b, q')$.

The oracle machine is a transducer over $\Sigma$ and $2$; the action machine a deterministic FST over $2$ and $\Gamma$. Each transducer can be canonically decomposed into its oracle and action machines:

Proposition 2. $\mathcal{R}[T] = \mathcal{R}[T^\alpha] \circ \mathcal{R}[T^\beta]$ where $\circ$ denotes relational composition. Note that the oracle machine is independent of the outputs in the original transducer; in particular, a transducer where only the outputs are changed has the same oracle machine. Intuitively, the action machine starts at the initial state the original transducer, automatically follows transitions from resting and skip states, and uses the bit string from the oracle machine as an oracle—hence the name—to choose which transition to take from a choice state; in this process it emits the outputs it traverses.

Example 1. Figure 1 shows a Kleenex program (see Section 3), the associated transducer and its decomposition into oracle and action machines.

Observe that if there is a path $q \xrightarrow{u/v} q'$ then $u$ uniquely identifies the path from $q$ to $q'$ in a transducer and, furthermore, in an oracle machine so does $u$.

We write $q \xrightarrow{u/v} q''$ if the path $q \xrightarrow{u/v} q''$ if the path $q \xrightarrow{u/v} q''$ does not contain an $\epsilon$-loop, that is a subpath $q \xrightarrow{u'/v'} q'$ where $u\overline{v} = \epsilon$. Paths without $\epsilon$-loops are called nonproblematic paths [29].

Definition 5 (Greedy semantics). The greedy semantics of a transducer $T$ is $G[T] = \mathcal{R}[T^\alpha] \circ G[T^\beta]$ where

$$G[T^\alpha] = \{(\tau, \tau') | q \xrightarrow{u/v} q' \land \forall \bar{u}', \bar{v}' q \xrightarrow{u'/v'} q'' \land \overline{\bar{u}} \implies \overline{\bar{v}} \leq \overline{\bar{v}'}, \bar{v} \leq \bar{v}'\}$$

and $\leq$ denotes the lexicographic ordering on bit strings.

Given input string $s$, the greedy semantics chooses the lexicographically least path in the transducer accepting $s$ and outputs the corresponding output symbols encountered along the path. The restriction to nonproblematic paths ensures that there are only finitely many paths accepting $s$ and thus the lexicographically least amongst them exists, if $s$ is accepted at all. We write $q \xrightarrow{u/v}_{\text{min}} q'$ if $q \xrightarrow{u/v} q'$ is the lexicographically least nonproblematic path from $q$ to $q'$.

A transducer $T$ over $\Sigma$ and $\Gamma$ is single-valued if $\mathcal{R}[T]$ is a partial function from $\Sigma^*$ to $\Gamma^*$.

Proposition 3. Let $T$ be an transducer over $\Sigma$ and $\Gamma$.

- $G[T]$ is a partial function from $\Sigma^*$ to $\Gamma^*$.
- $G[T] = \mathcal{R}[T]$ if $T$ is single-valued.

The greedy semantics can be thought of as a disambiguation policy for transducers that conservatively extends the standard semantics for single-valued transducers to a deterministic semantics for arbitrary transducers.

3. Kleenex

Kleenex is a language for compactly and conveniently expressing transducers.

3.1 Core Kleenex

Core Kleenex is a grammar for directly coding transducers.

Definition 6 (Core Kleenex syntax). A Core Kleenex program is a nonempty list $p = \text{do} d_1 \ldots d_n$ of definitions $d_i$, each of the form $N := t$, where $N$ is an identifier and $t$ is generated by the grammar

$$t ::= \epsilon | N \mid a \, N' \mid \text{"do"} \, N' \mid N_0 \mid N_1$$

where $a \in \Sigma$ and $b \in \Gamma$ for given alphabets $\Sigma, \Gamma$, e.g. some character set. $N$ ranges over some set of identifiers. The identifiers occurring in $p$ are called nonterminals. There must be at most one definition of each nonterminal, and every occurrence of a nonterminal must have a definition.

Definition 7 (Core Kleenex transducer semantics). A transducer associated with Core Kleenex program $p$ for nonterminal $N \in N$ is

$$T_p(N) = (\Sigma, \Gamma, N[q^I], N, q^I, E)$$

Kleenex is a contraction of Kleene and expression in recognition of the fundamental contributions by Stephen Kleene to language theory.
main := (num /\n/)*
num := digit(1,3)
\( (\text{"\$\" } \text{digit}(3))\)+
digit := /\n/

Let \( \text{Reach}(N) = \{ \bar{N}_k \mid N \xrightarrow{\ell} \ldots \xrightarrow{\ell} \bar{N}_k \} \) be the nonterminal sequences reachable from \( N \) along transitions in \( E \). The transducer \( T_p \) associated with \( p \) is \( (\Sigma, \Gamma; R, N, \epsilon, E|_R) \) where \( R = \text{Reach}(N) \) for designated start symbol \( N \) and \( E|_R \) is \( E \) restricted to \( R \). The (greedy) semantics of \( p \) is the greedy semantics of \( T_p \): \( G[p] = G[T_p] \).

The following proposition justifies calling \( T_p \) a transducer.

**Proposition 4.** Let \( p \) be a well-formed Standard Kleenex program, with \( T_p \) as defined above. Then \( R \) is finite, and \( T_p \) is a transducer, that is normalized FST.

**Proof.** (Sketch) \( \text{Reach}(N) \) consists of all the nonterminal suffixes of sentential forms of left-most derivations of \( p \) considered as a context-free grammar. In well-formed Kleenex programs, their maximum length is bounded by \( |N| \). It is easy to check that every state in \( R \) is either a resting, skip, choice or final state.

Observe that the transducer associated with a Kleenex program can be exponentially bigger than the program itself.

Since a transducer has a straightforward representation in Core Kleenex, the construction of \( T_p \) provides a translation of a well-formed Standard Kleenex program into Core Kleenex. For example, the Kleenex program on the left translates into the Core Kleenex program on the right:

\[
M' := M'|N \\
M' := N.N_a \\
N_a := a \\
N := N'|N_e \implies N' := a.N_e \\
N' := N_k.N \\
N := N'|N_e \\
N_e := b \\
N_e := \epsilon
\]

### 3.3 The Full Surface Language

The full surface syntax of Kleenex is obtained by extending Standard Kleenex with the following term-level constructors, none of which increase the expressive power:

\[
t := \ldots | v^n | t/e/ | t\cdot | t_0.t_1 | t_0\cdot t_1 | t* | t? | t\{(n)\} | t\{\{n\}\} | t\{\{n,m\}\}
\]

where \( v \in \Gamma^* \), \( n, m \in \mathbb{N} \), and \( e \) is a regular expression. The terms \( t_0.t_1 \) and \( t_0\cdot t_1 \) desugar into \( N_0.N_1 \) and \( N_0\cdot N_1 \), respectively, with additional productions \( N_0 := t_0 \) and \( N_1 := t_1 \) for new nonterminals \( N_0, N_1 \). The term \( v^n \) is shorthand for a sequence of outputs.

Regular expressions are special versions of Kleenex terms without nonterminals. They desugar to terms that output the matched
input string, i.e. `/e/` desugars by adding an output symbol "e" after every input symbol `a` in `e`. For example, the regular expression `/a*b\{n,m\}` desugars to `(a*b)` if `\{n,m\}` is bounded, which can then be further desugared.

The operators `*`, `+` and `?` desugar to their usual meaning as regular operators, as do the repetition operators `{n}`, `{\{n\}}`, `{\{n,m\}}`, and `{\{n,m\}}`. Note that they desugar into their greedy variants where matching a subexpression is preferred over skipping it. For example:

```
M := (a * b)*  \implies M := (a * b)N'  \implies N' := a * b N' \epsilon
```

Lazy variants can be encoded by making `e` the left rather than the right choice of an alternative.

### 3.4 Register Update Actions

By viewing `Γ` as an alphabet of effects, we can extend the expressivity of Kleenex beyond rational functions [13]. Let `X` be a countable set, and assume that there is an effective partial action `Γ × X → X`. It is simple to define a deterministic machine implementing the function `Γ^* × X → X` by successively applying a list of actions to some starting state `X`. Any Kleenex program then denotes a function `Γ^* × X → X` by composing its greedy semantics with such a machine. If we can implement the pure transducer part in a streaming fashion, then a state `X` can be maintained on-the-fly by interpreting output actions as soon as they become available.

Let `X = (Γ^*)^n × (Γ^*)^m` for some `n`, representing a non-empty stack of output strings and `n` string registers. The transducer output alphabet is extended to `Γ[{push, pop0, ..., popn}, write0, ..., writeln]`, with actions defined by

```
(tw0, ..., vn) · a = ((ta)w0, ..., vn)  (a ∈ Γ)
(tw0, ..., vn) · push = ((ε)w0, ..., vn)
(tw0, ..., vn) · popi = (w0, ..., tw, ..., vn) (|tw| > 0)
(tw0, ..., vn) · writei = ((tv0)w0, ..., vn)
```

The bottom stack element can only be appended to and models a designated register—popping it is undefined. The stack and the variables can be used to perform complex string interpolation. To access the extended actions, we extend the surface language:

```
t ::= ... | R ⊕ t | tR | [R] · (\{\{\}\}^*) | [R + s] · (\{\{\}\}^*)
```

where `R` ranges over `register names` standing for indices.

The term `R ⊕ t` desugars to "push` t" `push`, and the term `tR` desugars to `writeR`". The term `[R := x1 \ldots xn]` desugars to "push`t1 \ldots tn` `pop0n`, where `t1 := writeR1` if `x1 := Rn`, and `t1 := x1` otherwise. Finally, `[R + s]` desugars to `[R := x1 \ldots xn]`. Thus all streaming string transducers (see Section 5) can be coded. As an example, the following program swaps two input lines by storing them in registers `a` and `b` and outputting them in reverse order:

```
main := "online bold line !b !a
line /= /\n|/\n/\n
where the first line above desugars to

```
main := "push" line "pop_{a}" "push" line "pop_{b}"

"write_{a}" "write_{b}"
```

### 4. Streaming Simulation

As we have seen, every Kleenex program has an associated transducer, which can be split into oracle and action machines. The action machine is a straightforwardly implemented deterministic FST. The oracle machine is nondeterministic, however: The key challenge is how to (deterministically) find and output the lexicographically least path that accepts a given input string. In this section we develop an efficient oracle machine simulation algorithm that inputs a stream of symbols and streams the output bits almost as early as possible during input processing.

### 4.1 Path Trees

Given an oracle machine `T^C` as in Definition 4, consider input `s` such that `q = u/v min q'` where `|u| = s`. Recall that `q = u/v min q'` uniquely identifies a path from `q` to `q'` in `T^C`, which is furthermore asserted to be the lexicographically minimal amongst all nonproblematic paths from `q` to `q'`.

**Proposition 5** (Path decomposition). Assume `q = u/v min q'`. For every prefix `s'` of `|u|` there exist unique `u', v', v'', q'` such that:

```
q = u/v min q'  \iff  u/v = u' v'' v'' = v.
```

**Proof.** Let `u'` be the longest prefix of `u` such that `|u'| = s'` and let `q = u'/v' min q'` be the path from `q` determined by `u'`. (Such a prefix must exist.) Claim: This is the `q'` in the proposition.

1. `q'` is a resting state. If it were not, we could transition on `ε, ε` or `ε` resulting in a longer prefix `w` with `|w| = s''`.

2. `q = u'/v' min q'`. If any of these subpaths were not lexicographically minimal, we could replace it with one that is lexicographically less, resulting in a path from `q` to `q'` that is lexicographically less than `q - u/v min q'`, contradicting our assumption `q = u/v min q'`.

After reading input prefix `s'` we need to find the above `q = u'/v' min q'` where `|u'| = s'`. Since we do not know the remaining input yet, however, we maintain all paths `q = u'/v' min q'` for any resting state `q` such that `|u'| = s`.

**Definition 10** (Path tree). Let `T^C` be given. Its `Path tree` `P(s)` for `s` is the set of paths `\{q = u/v min q' | |u| = s\}`.

Consider a transducer as a directed labeled graph where the nodes are transducer states indexed by the strings reaching them, `{q_s | \exists u, v, q = u/v min \epsilon ∧ |u| = s}`, and the edges are the corresponding transitions, `{q_s \rightarrow q_{u/v} | q_{u/v} min q'}`. It can be seen that `P(s)` is a subgraph that forms a non-null rooted edge-labeled binary tree. The stem of `P(s)` is the longest path in this tree from `q_{\epsilon}` to some `q_{s'}` for a prefix `s'` of `s` only involving nodes with at most one child. The leaves of `P(s)` are the states `q_s` such that `q_s` is reachable, in lexicographic order of the paths reaching them from `q_{\epsilon}`.

**Example 2.** Recall the oracle machine for the decimal converter in the lower left of Figure 1. Its path tree for input `a` is shown in the upper left of Figure 2. The nodes are subscripted with the length of the input prefix rather than the input prefix itself. Note that the leaf states are listed from top to bottom in lexicographic order of their paths reaching them. This means that the top state is the prime candidate for being `q'` in Proposition 5. If the remainder of the input is not accepted from it, though, the other leaf states take over in the given order.
Algorithm 1

The upper right in Figure 2 shows $\epsilon$-leaf states 4 and 9, which have an $\alpha$-transition, and building the $\epsilon$-closure as a binary tree. It prunes branches either because they reach a state already reached by a lexicographical lower path (state 6) or because the leaf does not have transition on $a$ (state 13). The algorithm outputs 0 after reading the first $a$ since 0 is the sequence of output bits on the stem of the path tree. It does not output anything after reading the second $a$ since $P(\alpha\alpha)$ has the same stem as $P(\alpha)$. Theorem 1. Let $T$ be given. Write $\mathcal{L}[q]$ for $\mathcal{L}[T^*]$ where $T^*$ is $\mathcal{T}$, but with q as initial state instead of $q^*$. A state q is covered by $\{q_1, \ldots, q_n\}$ if $\mathcal{L}[q] \subseteq \cup \mathcal{L}[q_i]$. A path tree $P(s')$ with lexicographically ordered leaves $\{q_1, \ldots, q_n\}$ is cover-free if no q is covered by $\{q_1, \ldots, q_{n-1}\}$. $T$ is cover-free if $P(s')$ is cover-free for all $s' \in \Sigma^*$. Theorem 1. Let $T$ be cover-free. Then Algorithm 1 with Algorithm 2 for incremental path tree recomputation is an optimally streaming algorithm for $G[T^*]$ that runs in time $O(mn)$, where $m = |T^*|$ and $n$ is the length of the input string.

**Proof.** (Sketch) Algorithm 2 can be implemented to run in time $O(m)$ since it visits each transition in $T^*$ at most once and pruning can be amortized: every deallocation of an edge can be charged to $O(C)$. Since $T^*$ is at most $O(C)$ times optimal streaming follows from a generalization of the proof of optimal streaming for regular expression parsing [32].

The algorithm can be made optimally streaming for all oracle transducers by also pruning leaf states that are covered by other leaf states in Step 6 of Algorithm 2. Coverage is PSPACE-complete, however. Eliding the coverage check does not seem to make much of a difference to the streaming behavior in practice.

## 4.2 Basic Simulation Algorithm

The basic streaming simulation algorithm works as follows:

**Algorithm 1** (Basic streaming algorithm). Let $s = a_1 \ldots a_n \in \Sigma^*$ be the input string.
1. for $i = 1$ to $n$ do
2. if $P(a_1 \ldots a_i) = \emptyset$ then
3. terminate with failure (input rejected)
4. if stem($P(a_1 \ldots a_i)$) longer than stem($P(a_1 \ldots a_{i-1})$) then
5. emit the output bits on the stem extension
6. if $P(a_1 \ldots a_n)$ contains path to $q'$ then
7. if path tree contains at least one branch, emit output bits on path from highest binary ancestor to $q'$
8. terminate with success (input accepted)
9. else
10. terminate with failure (input rejected)

The critical step in the algorithm is incrementally computing the path tree for $s'$ from the path tree for $s$.

**Algorithm 2** (Incremental path tree computation). Let $P$ be $P(s')$ for some prefix $s'$ of the input string, and let $\{q_0, \ldots, q_n\}$ be its leaves in lexicographic order of the paths reaching them. Upon reading $a$, incrementally compute $P(s'a)$ as follows.
1. for $q = q_0$ to $q_n$ do
2. compute $P_q(a)$, the path tree of lexicographically least $(u/v)$ paths with $u = a$ from $q$ to resting states, but excluding resting states that have been reached in a previous iteration
3. if $P_q(a)$ is non-empty then
4. replace leaf node $q$ in $P$ by $P_q(a)$
5. else
6. prune branch from lowest binary ancestor to leaf node $q$; if binary ancestor does not exist, then terminate with failure (input rejected)

**Example 3.** The upper right in Figure 2 shows $P(\alpha\alpha)$ for the decimal converter. Observe how it arises from $P(\alpha)$ by extending leaf states 4 and 9, which have an $\alpha$-transition, and building the $\epsilon$-closure as a binary tree. It prunes branches either because they reach a state already reached by a lexicographical lower path (state 6) or because the leaf does not have transition on $a$ (state 13). The algorithm outputs 0 after reading the first $a$ since 0 is the sequence of output bits on the stem of the path tree. It does not output anything after reading the second $a$ since $P(\alpha\alpha)$ has the same stem as $P(\alpha)$. Theorem 1. Let $T$ be given. Write $\mathcal{L}[q]$ for $\mathcal{L}[T^*]$ where $T^*$ is $\mathcal{T}$, but with q as initial state instead of $q^*$. A state q is covered by $\{q_1, \ldots, q_n\}$ if $\mathcal{L}[q] \subseteq \cup \mathcal{L}[q_i]$. A path tree $P(s')$ with lexicographically ordered leaves $\{q_1, \ldots, q_n\}$ is cover-free if no q is covered by $\{q_1, \ldots, q_{n-1}\}$. $T$ is cover-free if $P(s')$ is cover-free for all $s' \in \Sigma^*$. Theorem 1. Let $T$ be cover-free. Then Algorithm 1 with Algorithm 2 for incremental path tree recomputation is an optimally streaming algorithm for $G[T^*]$ that runs in time $O(mn)$, where $m = |T^*|$ and $n$ is the length of the input string.

**Proof.** (Sketch) Algorithm 2 can be implemented to run in time $O(m)$ since it visits each transition in $T^*$ at most once and pruning can be amortized: every deallocation of an edge can be charged to its allocation. Algorithm 1 invokes Algorithm 2 $n$ times. Optimal streaming follows from a generalization of the proof of optimal streaming for regular expression parsing [32].

The algorithm can be made optimally streaming for all oracle transducers by also pruning leaf states that are covered by other leaf states in Step 6 of Algorithm 2. Coverage is PSPACE-complete, however. Eliding the coverage check does not seem to make much of a difference to the streaming behavior in practice.

## 5. Determinization

NFA simulation maintains a set of NFA states. This is the basis of compiling an NFA into a DFA: precompute and number the
set of all NFA state sets reachable by any input from the initial NFA state, observing that there are only finitely many such sets. In the transducer simulation in Section 4 path trees play the role of NFA state sets. The corresponding determination idea does not work for transducers, however: \(\{P(s) \mid s \in \Sigma^*\}\) is in general infinite. For example, for the oracle machine in Figure 1, the trees \(P(a^i)\) all have the same stem, but contain paths with bit strings of length proportional to \(n\). This is inherently so. A single-valued transducer can be transformed effectively \([12, 66]\) into a form of deterministic finite-state transducer if its relational semantics is subsequential \([13, 53]\), but nondeterministic finite state transducers in general are more properly expressive than their deterministic counterparts. We can factor a path tree into its underlying full binary tree and the labels associated with the edges, though. Since there are only finitely many different such trees, we can achieve determination to transducers with registers storing the potentially unbounded label data.

**Definition 12** (Streaming String Transducer \([4]\)). A deterministic streaming string transducer \((SST)\) over alphabets \(\Sigma, \Gamma\) is a tuple \(S = (X, Q, q^0, F, \delta^1, \delta^2)\) where

- \(X\) is a finite set of register variables;
- \(Q\) is a finite set of states;
- \(F\) is a partial function \(Q \rightarrow (\Gamma \cup X)^*\) mapping each final state \(q \in \text{dom}(F)\) to a word \(F(q) \in (\Gamma \cup X)^*\) such that each \(x \in X\) occurs at most once in \(F(q)\);
- \(\delta^1\) is a transition function \(Q \times \Sigma \rightarrow Q\);
- \(\delta^2\) is a register update function \(Q \times \Sigma \rightarrow (X \rightarrow (\Gamma \cup X)^*)\) such that for each \(q \in Q, a \in \Sigma\) and \(x \in X\), there is at most one occurrence of \(x\) in the multiset of strings \(\delta^2(q, a)(y)\) for \(y \in X\).

A configuration of an SST \(S = (X, Q, q^0, F, \delta^1, \delta^2)\) is a pair \((q, \rho)\) where \(q \in Q\) is a state, and \(\rho: X \rightarrow \Gamma^*\) is a valuation. A valuation extends to a monoid homomorphism \(\hat{\rho}: (X \cup \Gamma)^* \rightarrow \Gamma^*\) by setting \(\rho(x) = x\) for \(x \in \Gamma\). The initial configuration is \((q^0, \rho^0)\) where \(\rho^0(x) = \epsilon\) for all \(x \in X\).

A configuration steps to a new configuration given an input symbol \(\delta((q, \rho), a) = (\delta^1(q, a), \hat{\rho} \circ \delta^2(q, a), \delta^2(q, a, u))\). The transition function extends to a transition function on words \(\delta^\ast((q, \rho), \epsilon) = (q, \rho)\) and \(\delta^\ast((q, \rho), u) = \delta^\ast((q, \rho), a)\).

Every SST \(S\) denotes a partial function \(\mathcal{F}[S]: \Sigma^* \rightarrow \Gamma^*\) where for any \(u \in \Sigma^*\) such that \(\delta^\ast((q^0, \rho^0), u) = (q', \rho')\), we define

\[
\mathcal{F}[S](u) = \begin{cases} 
\hat{\rho}(F(q')) & \text{if } q' \in \text{dom}(F) \\
\text{undefined} & \text{otherwise}
\end{cases}
\]

In the following, let \(X = \{r_p \mid p \in 2^n\}\) be a set of registers.

**Example 4.** Consider the bottom left tree in Figure 2. This is the reduced register tree obtained from the path tree above it. The evaluation map \(\rho\) can be seen below it, where register subscripts denote their position in the register tree. In the middle is the result of extending the register tree using Algorithm 2. Reducing this again yields the tree on the right. The update map \(\kappa\) is shown below it—note that the range of this map is mixed register/bit sequences.

**Proposition 6.** Let \(\mathcal{T}^{C}\) be given, and let \(P = P(s), P' = P(s)\) for all state \(s\). Let \(R = R(a)\) be \(\mathcal{R}(P)\) and \(R'(\rho) = \mathcal{R}(P')\) for some \(s\) and \(a\). Then \(R' = R_a\) and \(\rho' = \rho \circ R_a\).

**Theorem 2.** Let \(\mathcal{T}^{C}\) be an oracle machine of size \(m\). There is an SST \(S\) with \(O(2^{m \log m})\) states such that \(\mathcal{F}[S] = \mathcal{G}[T]\).

**Proof.** Let \(Q_S = \{R(P(s)) \mid s \in \Sigma^*\}\) and \(q_S = R_0\), where \(R_0\) is the single-leaf binary tree with leaf \(q_T\). The set of registers \(X_S\) is the finite subset of register variables occurring in \(Q_S\). The transition maps are given by \(\delta^2_k(R, a) = R_a\) and \(\delta^1_k(R, a) = R_k/a\). For any \(R \in Q_S\), define the final output \(F_S(R)\) to be the sequence of registers on the path from the root to the final state \(q_T\). If \(R\) contains it as a leaf; otherwise let \(F_S(R)\) be undefined. Let \(F_S(R_0) = \theta\) if \(q_T^\ast = \epsilon\) and otherwise let \(F_S(R_0)\) be undefined.

Correctness follows by showing \(\delta^\ast((R_0, \rho^0), u) = \mathcal{R}(P(u))\) for all \(u \in \Sigma^*\). We prove this by induction, applying Proposition 6 in each step. For the case \(u = \epsilon\) correctness follows by the definition of \(F_S(R_0)\).

The upper bound follows from the fact that there are at most \(C_{k-1}(k-1)! = O(2^{m \log m})\) full binary trees with \(k\) pairwise distinct leaves where \(k\) is the number of nesting states in \(T^\ast\) and \(C_{k-1}\) is the \((k-1)\)-st Catalan number.

**Example 5.** The oracle machine in Figure 1 yields the SST in Figure 3. The states 1 and 2 are identified by the left and right reduced trees, respectively, in the bottom of Figure 2.

**Corollary 1.** The SST \(S\) for \(\mathcal{T}^{C}\) can be implemented to execute in time \(O(nm)\) where \(m = |T^\ast|\).

**Proof.** (Sketch) Use a data structure for imperatively extending a string register, \(r: \pi \mapsto s\), in amortized time \(O(n)\) where \(n\) is the size of \(s\), independent of the size of the string stored in \(r\). The result then follows from the fact that the steps in Algorithm 2 can be implemented in the same amortized time.

In practice, the compiled version of the SST is much more efficient—roughly one to two orders of magnitude faster—than streaming simulation since it compiles away the interpretive overhead of explicitly managing the binary trees underlying path trees and employs machine word-level parallelism by operating on bit strings in fewer registers rather than many edges each labeled by at most one bit.

**6. Implementation and Benchmarks**

Our implementation\(^\text{1}\) compiles the action machine and the oracle SST to machine code via C. We have implemented several optimizations which are orthogonal to the underlying principles behind our compilation from Kleenex via transducers to SSTs:

**Inlining of output actions** The action machine and the oracle SST need to be composed. We can do this at runtime by piping the SST output to the action machine, or we can apply a form of deforestation \([70]\) to inline the output actions directly into the SST. This is straightforward since the machines are deterministic.

---

\(^1\)Source code and benchmarks available at http://kleenexlang.org/
**Constant propagation** The SSTs generated by the construction underlying Theorem 2 typically contain many constant-valued registers (e.g. most registers in Figure 3 are constant). We eliminate these using constant propagation: compute reaching definitions by solving a set of data-flow constraints.

**Symbolic representation** A more succinct SST representation is obtained by using a symbolic representation of transitions where input symbols are replaced by predicates and output symbols by terms indexed by input symbols. This is a straightforward extension of similar representations for automata [72] and transducers [66–69]. Our implementation uses simple predicates in the form of byte ranges, and simple output terms represented by byte-indexed lookup tables. We refer the reader to the cited literature for the technical details of symbolic transducers.

**Finite lookahead** Symbolic FSTs with bounded lookahead have been shown to reduce the state space when representing string encoders [22, 67, 69]. We have implemented a form of finite lookahead in our SST representation. Opportunities for lookahead is detected by the compiler, and arise in the case where the program contains a string constant with length above one. In this case a lookahead transition is used to check once and for all if the string constant is matched by the input instead of creating an SST state for each symbol. This may in some cases reduce the size of the generated code since we avoid tabulating all states of the whole program for every prefix of the string constant.

We have run comparisons with different combinations of the following tools:

- **RE2**, Google’s regular expression C++ library [62].
- **RE2j**, a recent re-implementation of RE2 in Java [63].
- **GNU awk** and **GNU sed**, programming languages and tools for text processing and extraction [60].
- **Oniglib**, a regular expression library written in C++ with support for different character encodings [38].
- **Ragel**, a finite state machine compiler with multiple language backends [65].

In addition, we implemented test programs using the standard regular expression libraries in the scripting languages Perl [71], Python [41], and Tcl [73].

The benchmark suite, Kleenex programs, and version numbers of libraries used can be found at [http://kleenexlang.org](http://kleenexlang.org).

**Meaning of plot labels** Kleenex plot labels indicate the compilation path, and follow the format `-0 [clang/gcc]`. 0/3 indicates whether constant propagation was disabled/enabled. 1a indicates whether lookahead was enabled. clang/gcc indicates which C compiler was used. The last part indicates that custom register updates are disabled, in which case we generate a single fused SST as described in Section 6.3. These are only run with constant propagation and lookahead enabled.

**Experimental setup** The benchmark machine runs Linux, has 32 GB RAM and an eight-core Intel Xeon E3-1276 3.6 GHz CPU with 256 KB L2 cache and 8 MB L3 cache. Each benchmark program was run 15 times, after first doing two warm-up rounds. All C and C++ files have been compiled with -O3.

**Difference between Kleenex and the other implementations** Unless otherwise stated, the structure of all the non-Kleenex implementations is a loop that reads input line by line and applies an action to the line. Hence, in these implementations there is an interplay between the regular expression library used and the external language, e.g. RE2 and C++. In Kleenex, line breaks do not carry any special significance, so the multi-line programs can be formulated entirely within Kleenex.

**Ragel optimization levels** Ragel is compiled with three different optimization levels: T1, F1, and G2. “T1” and “F1” means that the generated C code should be based on a lookup-table, and “G2” means that it should be based on C goto statements.

**Kleenex compilation timeout** On some plots, some versions of the Kleenex programs are not included. This is because the C compiler times out (after 30 seconds). As we can sometimes observe, the resulting C code can explode in some cases. The two worst-case exponential blow-ups in generating transducers from Kleenex and then generating SSTs implemented in C code from transducers are inherent, though, and as such can be considered a feature of Kleenex: tools based on finite machines with no or limited nondeterminism support such as Ragel would require hand-coding a potentially huge machine that Kleenex generates automatically.4

### 6.1 Baseline

The following two programs are intended to give a baseline impression of the performance of Kleenex programs.

- **flip_ab** The program flip_ab swaps “a”s and “b”s on all its input lines. In Kleenex it looks like this:

  ```
  main := ("b" ~a/ | "a" ~/b/ | /a/b/)*
  ```

  We made a corresponding implementation with Ragel, using a while-loop in C to get each new input line and feed it to the automaton code generated by Ragel.

  Implementing this functionality with regular expression libraries in the other tools would be an unnatural use of them, so we have not measured this.

4 We have found it excessively difficult to employ Ragel in some use cases with a natural nondeterministic specification.
We evaluated the Kleenex implementation along with two other implementations using Perl and Python. The performance can be seen in Figure 6. Both Perl and Python are significantly slower than all of the Kleenex implementations; the problem is tricky to solve with regular expressions unless one reads the input right-to-left.

**IRC protocol handling** The following Kleenex program parses the IRC protocol as specified in RFC 2812.\(^5\) It follows roughly the output style described in part 2.3.1 of the RFC. Note that the Kleenex source code and the BNF grammar in the RFC are almost identical. Figure 7 shows the throughput on 250 MiB data.

```
main := (message | "Malformed line: " /[^\r\n]*\r?\n//)*
message := (\/: "Prefix: " prefix "\n" "Command: " command "\n" "Parameters: " params? "\n" "cr"
command := letter+ | digit[3]
prefix := servername | nickname ((/|/ user)? /6/ host )?
user := /([^\n]\r]?\r?\n// Missing \x00
middle := nospcrlfc1 ( /:/ | nospcrlfc1 )*
params := (\/: / middle "",*){14} (\/: / trailing )?
| (\/: / middle ){14} (\/: / trailing )?
trailing := (\/: / | / | nospcrlfc1)*
nickname := (letter | special) (letter | special | digit){,10}
host := hostname | hostaddr
servername := hostname
hostname := shortname ( / | | shortname)*
hostaddr := ip4addr
shortname := (letter | digit) (letter | digit | /-)*
| (letter | digit)*
ip4addr := (digit{1,3} /\./ ){3} digit{1,3}
```

**CSV rewriting** The program csv_project3 deletes all columns but the 2nd and 5th from a CSV file:

```
main := (row /\n//)*
col := /[^\n]\r?\n//
row := (col //) col "\t" /"~" (col //)
| (col //) col "\t" /"~" col
```

Various specialized tools that can handle this transformation are included in Figure 8; GNU cut is a command that splits its input on certain characters, and GNU awk has built-in support for this type of transformation.

\(^5\)https://tools.ietf.org/html/rfc2812
6.3 With or Without Action Separation

One can choose to use the machine resulting from fusing the oracle and action machines when compiling Kleenex. Doing so results in only one process performing both disambiguation and outputting, which in some cases is faster and in other cases slower. Figures 8, 9, and 11 illustrate both situations. It depends on the structure of the problem whether it pays off to split up the work into two processes; if all the work happens in the oracle machine and the action machine does nearly nothing, then the added overhead incurred by the process context switches becomes noticeable. On the other hand, in cases where both machines perform much work, the fact that two CPU cores can be utilized in parallel speeds up execution. This is more likely once Kleenex has support for actions that can perform arbitrary computations, e.g. in the form of embedded C code.

7. Use Cases

We briefly touch upon various use cases—natural application scenarios—for Kleenex.

JSON logs to SQL We have implemented a Kleenex program that transforms a JSON log file into an SQL insert statement. The program works on the logs provided by Issuu.

The Ragel version we implemented outperforms Kleenex by about 50% (Figure 9), indicating that further optimizations of our SST construction should be possible.

Apache CLF to JSON The Kleenex program below rewrites Apache CLF log files into a list of JSON records:

```plaintext
main := 
loglines := * (logline | slash)* logline / slash/ logline := 
"host" host sep "userid" userid sep "authuser" authuser sep "timestamp" timestamp sep request sep "code" code sep bytes sep referrer sep "useragent" useragent

```

This is a re-implementation of a Ragel program. Figure 10 shows the benchmark results. The versions compiled with clang are not included, as the compilation timed out after 30 seconds. Curiously, the non-optimized Kleenex program is the fastest in this case.

ISO datetime objects to JSON Inspired by an example in [30], the program isodatetime_to_json converts date and time

---

6 The line-based data set consists of 30 compressed parts; part one is available from http://labs.issuu.com/anodataset/2014-03-1.json.xz
7 https://httpd.apache.org/docs/trunk/logs.html#common
8 https://engineering.emcien.com/2013/04/5-building-tokenizers-with-ragel
Regular Expression Matching. Regular expression matching has different meanings in the literature.

For acceptance testing, the subject of automata theory where only a single bit is output, NFA-simulation and DFA-construction are classical techniques. Bille and Thorup [14] improve on Myers’ [46] log-factor improved classical NFA-simulation for regular expressions, based on tabling. They design an $O(kn)$ algorithm [15] with word-level parallelism, where $k \leq m$ is the number of strings occurring in an RE. The tabling technique may be promising in practice; the algorithms have not been implemented and evaluated empirically, though.

In subgroup matching as in PCRE [34], an input is not only classified as accepting or not, but a substring is returned for each sub-RE of interest. Subgroup matching exposes ambiguity in the RE. Subgroup matching is often implemented by backtracking over alternatives, which implements greedy disambiguation. Backtracking may result in exponential-time worst case behavior, however, even in the absence of inherently hard matching with backreferences [1]. Considerable human effort is usually expended to engineer REs used in practice to perform well anyway. More recently, REs designed to force exponential run-time behavior are used in algorithmic attacks, though [52, 56]. Some subgroup matching libraries have guaranteed worst-case linear-time performance based on automata-theoretic techniques, notably Google’s RE2 [62]. Intel’s Hyperscan [61] is also described as employing automata-theoretic techniques. A key point of Kleenex is implementing the natural backtracking semantics without actually performing backtracking and without requiring storage of the input.


Regular expression parsing. Full RE parsing, also called RE matching [29], generalizes subgroup matching to return a full parse tree. The set of parses are exactly the elements of a regular expression read as a type [29, 35]: Kleene-star is the (finite) list type constructor, concatenation the Cartesian product, alternation the type expression read as a product, also called RE matching [35, 42, 64] for Perl-style subgroup matching for greedy and POSIX disambiguation.

8. Discussion

We discuss related and future work by building Kleenex conceptually up from regular expression matching via regular expressions as types for bit-coded parsing to transducers and eventually grammars with embedded actions.

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Sulzmann and Lu [58] remark that POSIX is notoriously difficult to implement correctly and show how to use Brzozowski derivatives [20] for POSIX RE parsing.

**Regular expression implementation optimizations.** There are specialized RE matching tools and techniques too numerous to review comprehensively. We mention a few employing automaton optimization techniques potentially applicable to Kleenex, but presently unexplored. Yang, Manadhata, Horne, Rao, Ganapathy [75] propose an OBDD representation for subgroup matching and apply it to intrusion detection REs; the cycle counts per byte appear a bit high, but are reported to be competitive with RE2. Sidhu and Prasanna [54] implement NFAs directly on an FPGA, essentially performing NFA-simulation in parallel; it outperforms GNU grep, Brodie, Taylor, Cytron [18] construct a multistride DFA, which processes multiple input symbols in parallel, and devise a compressed implementation on stock FPGA, also achieving very high throughput rates. Likewise, Ziria employs tabled multistriding to achieve high throughput [55]. Navarro and Raffinot [48] show how to code DFAs compactly for efficient simulation.

**Finite state transducers.** From RE parsing it is a surprising short distance to the implementation of arbitrary nondeterministic finite state transducers (FSTs) [13, 43]. In contrast to the situation for automata, nondeterministic transducers are strictly more powerful than deterministic transducers: this, together with observable ambiguity, highlights why RE parsing is more challenging than RE acceptance testing.

As we have noted, efficient RE parsing algorithms operate on arbitrary NFAs, not only those corresponding to REs. Indeed, NFAs are not a particularly convenient or compact way of specifying regular languages: they can be represented by certain small NFAs with low tree width [36], but may be inherently quadratically bigger than automata, even for DFAs [24, Theorem 23]. This is why Kleenex employs well-formed context-free grammars, which are much more compact than regular expressions.

**Streaming string transducers.** We have shown in this paper that the greedy semantics of arbitrary FSTs can be compiled to a subclass of streaming string transducers (SSTs). SSTs extensionally correspond to regular transductions, functions implementable by 2-way deterministic finite-state transducers [4], MSO-definable string transductions [25] and a combinator language analogous to regular expressions [8]. The implementation techniques used in Kleenex appear to be directly applicable to all SSTs, not just the ones corresponding to FSTs.

**Symbolic transducers.** Veanes, Molnar, Mytkowicz [69] employ symbolic transducers [23, 68] in the implementation of the Microsoft Research languages BEK\(^\text{10}\) and BEX\(^\text{11}\) for multicore execution. These techniques can be thought of as synthesizing code that implements the transition function of a finite state machine not only efficiently, but also compactly. Tabling in code form (switch statement) or data form (lookup in array) is the standard implementation technique for the transition function. It is efficient when applicable, but not compact enough for large alphabets and multistrided processing. Kleenex employs basic symbolic transition. Compact coding of multistrided transitions is likely to be crucial for exploiting word-level parallelism—processing 64 bits at a time—in practice.

**Parallel transducer processing.** Allender and Mertz [3] show that the functions computable by cost register automata [7], which generalize the string monoid used in SSTs to admit arbitrary monoids and more general algebraic structures, are in NC and thus inherently parallelizable. This appears to be achievable by performing relational FST-composition by matrix multiplication on the matrix representation of FSTs [13], which can be performed by parallel reduction. This requires in principle running an FST from all states, not just the input state, on input string fragments. Mytkowicz, Musuvathi, Schulte [47] observe that there is often a small set of cut states sufficient to run each FST. This promises to be an interesting parallel harness for a suitably adapted Kleenex implementation running on fragments of very large inputs.

**Syntax-directed translation schemes.** A Kleenex program is an example of a syntax-directed translation scheme (SDTS) or a domain-specific stream processing language such as PADS [26, 27] and Ziria [55]. In these the underlying grammar is typically deterministic modulo short lookahead so that semantic actions can be executed immediately when encountered during parsing.

Kleenex is restricted to non-self-embedding grammars to avoid the matrix-multiplication lower bound on general context-free parsing [40]; it supports full nondeterminism without lookahead restriction, though. A key contribution of Kleenex is that semantic actions are scheduled no earlier than semantically permissible and no later than necessary.

9. **Conclusions**

We have presented Kleenex, a convenient language for specifying nondeterministic finite state transducers, and its compilation to machine code implementing streaming string transducers. Kleenex is comparatively expressive and performs consistently well. For complex regular expressions with nontrivial amounts of output it is almost always better than industrial-strength text processing tools such as RE2, Ragel, AWK, sed and RE-libraries of Perl, Python and Tcl in the evaluated use cases.

We believe Kleenex’s clean semantics, streaming optimality, algorithmic generality, worst-case guarantees and absence of tricky code and special casing provide a useful basis for

- extensions, specifically visibly push-down transducers [51, 59], restricted versions of backreferences and approximate regular expression matching [45, 74];
- known, but so far unexplored optimizations, such as multistriding, automata minimization and symbolic representation, hybrid FST-simulation/SST-construction;
- massively parallel (log-depth, linear work) processing.

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